



PATENT

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

APPLICANTS: KIM et al. EXAMINER: CHO, James Hyonchol
SERIAL NO.: 10/626,015 ART UNIT: 2819
FILED: July 24, 2003 DOCKET: 8836-201 (ID12049US)
ON: INTEGRATED CIRCUIT WITH ON-CHIP TERMINATION

Mail Stop AMENDMENT
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

AMENDMENT

This paper is responsive to the Office Action mailed on November 3, 2004. The Examiner's indication of allowable subject matter is gratefully acknowledged. The Examiner is requested to enter and consider the following amendments and remarks.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313 on the date indicated below:

Date: Jan. 19, 2005

By: E. Parker